

Hello — and thanks for taking a look at my work!

My name is Rich Kurz and I am an experienced graphics design professional. My philosophy has grown simpler through the years. Good design is not about me, but about us. I want to do good work that serves the needs of my client and that I am proud to put in my portfolio. This pdf shows some of my capabilities.

Note that all concepts and initial drawings are owned by Hewlett-Packard Co. The images in this document are my own and are under copyright to Rich Kurz.

This product note was the first piece that was all mine when I worked in HP Marcom. This pdf shows the cover, the first two front matter spreads, and a typical interior spread. Following those are some of the conceptualization sketches leading to the final design.

Of note is that this was produced before computer-based desktop publishing. ALL artwork was by hand and the boards that went to the printer were traditional paste-ups with a set of boards for each plate. It was a great foundation to carry into the modern world of cloud-based digital tools. I am very glad I was able to learn it that way.

Production involved developing the visual concept while working within a corporate standard layout template, specifying typesetting, doing hand paste-up and technical artwork, coordinating with sales engineers to get written content and screen shots, working with the printer's prepress department, and finally, press-checking...at whatever hour of the day or night the printer would run it.

Deliverables: 24 page, 8½" × 11" trimmed, saddle stitched, three spot colors plus semi-matte varnish on the cover
Separation-ready art boards for each spread and each plate
Printer pre-press coordination, press-proofing

I am available to discuss your design, illustration, marketing, and advertising needs. Let's talk!



Rich Kurz

A Guide to Cross-Domain Analysis

 HEWLETT
PACKARD

Solving Complex Digital Design Problems With The HP1631A/D

Product Note HP1631A/D-1

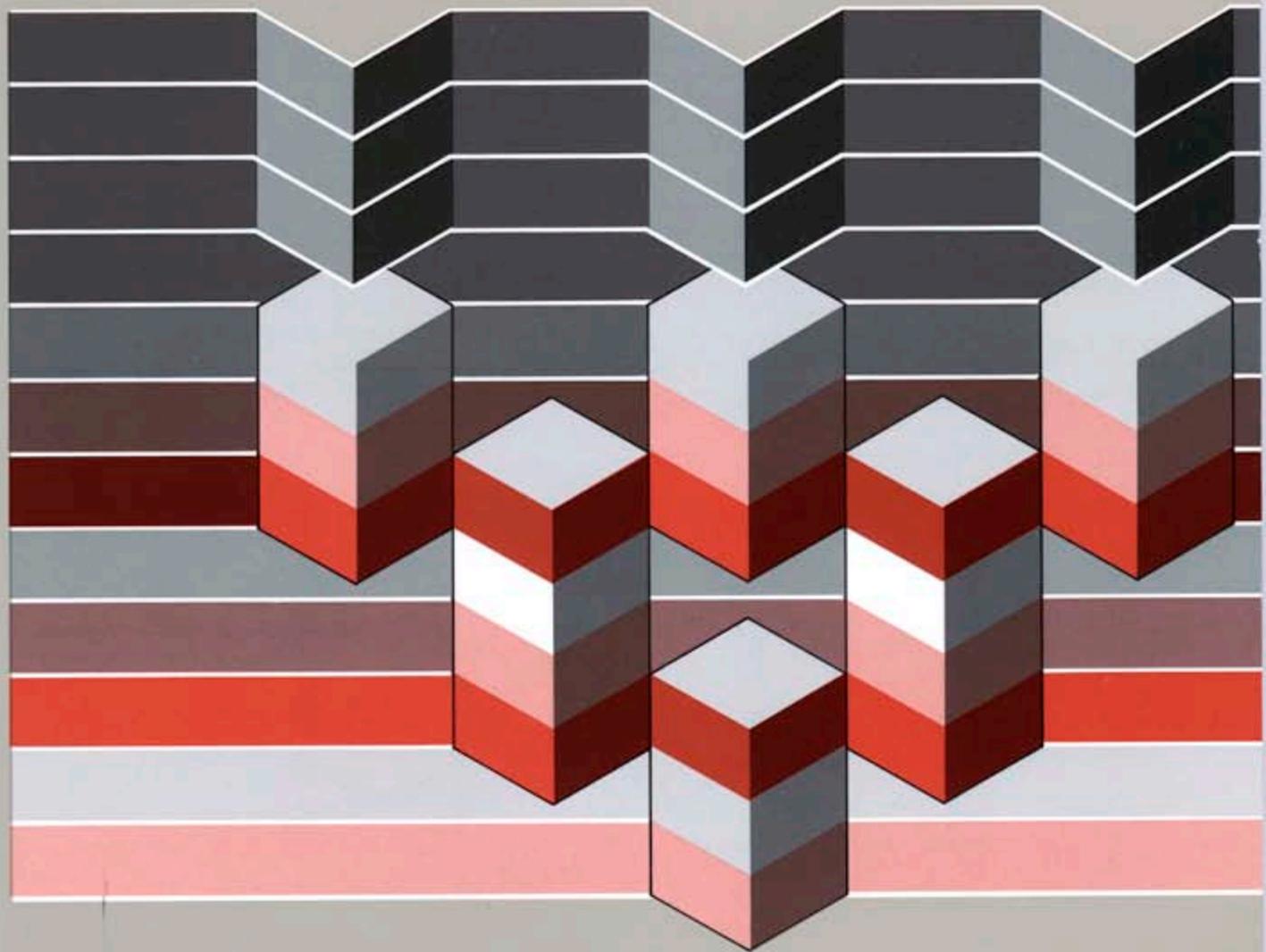


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FOR ADDITIONAL INFORMATION
on the HP1631A/D Logic Analyzer,
refer to the HP1631A/D technical
Data Sheet, publication number 5954-2614.

THE HP 1631A/D... INTRODUCTION

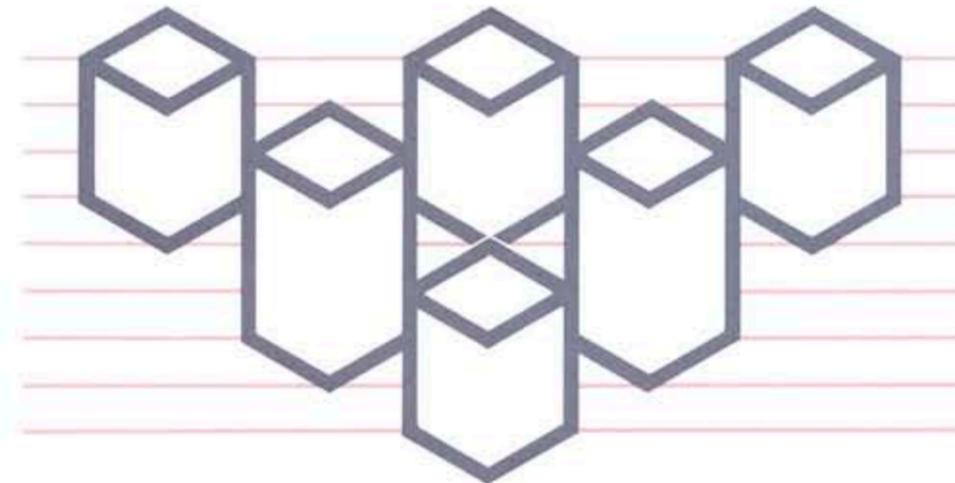
The One Tool For Every Phase Of The Test Of A Digital System

The HP 1631A/D is effective throughout the entire design cycle. A digital system has independent software, digital hardware, and analog hardware paths, with each requiring unique, diagnostic methods.

Integration at the functional level requires separate examination of 1) target system performance—the effectiveness with which resident code functions and hardware executes, and 2) total hardware performance—the

effectiveness with which digital hardware executes and the correct analog parameters result. Integration at the system level requires examining the entire system design with each of the separate designs operating interactively.

This design model is used throughout this product note to assist you in matching each of the HP 1631A/D's instrument functions to your measurement tasks.



THE HP 1631A/D... INTRODUCTION

The One Tool For Every Phase Of The Design Of A Digital System

FUNCTION PHASE

State Analysis

- Simultaneous capture of up to 43 channels of data
 - Synchronous to target system up to 25 MHz
 - 1k of memory per channel
- Measurement window positioning at any code location
 - States can be uniquely marked
 - Demultiplexing for code isolation
 - Resources for indexing and storage
- Software and hardware performance analysis
 - Histograms display execution time, usage time, and activity

Analog: Waveform Analysis

- Viewing
 - Single-shot capture for viewing infrequent events (200 megasamples per second digitizing rate)
 - Continuous viewing for general testing
 - Cumulative viewing for observing time and amplitude changes.
- Two channels of simultaneous acquisition (50 MHz bandwidth)
 - For correlating events
 - 1k of memory per channel
 - Pre-trigger viewing
- Time and voltage measurements, and a display of the answers

Analog: Post-processing

- Voltage and time statistics for hardware characterization
 - Mean and standard deviation to measure performance
 - Minimum and maximum values to guide next debug step
- Specify automatic single-shot measurements
- Search-and-then-stop acquisition under user-definable conditions.

Timing Analysis

- Up to 16 channels of simultaneous acquisition single-shot, continuous, or cumulative sampling
 - Asynchronous (100 MHz)
 - 1k of memory per channel
- Triggering on entering or leaving a pattern

- Time measurements with answers displayed
- Analyze glitches in timing diagrams

Timing: Post-processing

- Time-interval statistics for hardware characterization
 - Mean and standard deviation to measure performance
 - Minimum and maximum values to guide next debug step
- Specify automatic single-shot measurements
- Search-and-then-stop acquisition under user-definable conditions.

PERFORMANCE PHASE

Interactive: State And Timing, State And Analog

- User-definable specifications allow state, timing, or analog to arm simultaneous data acquisition by the remaining two.

Interactive: Timing And Analog

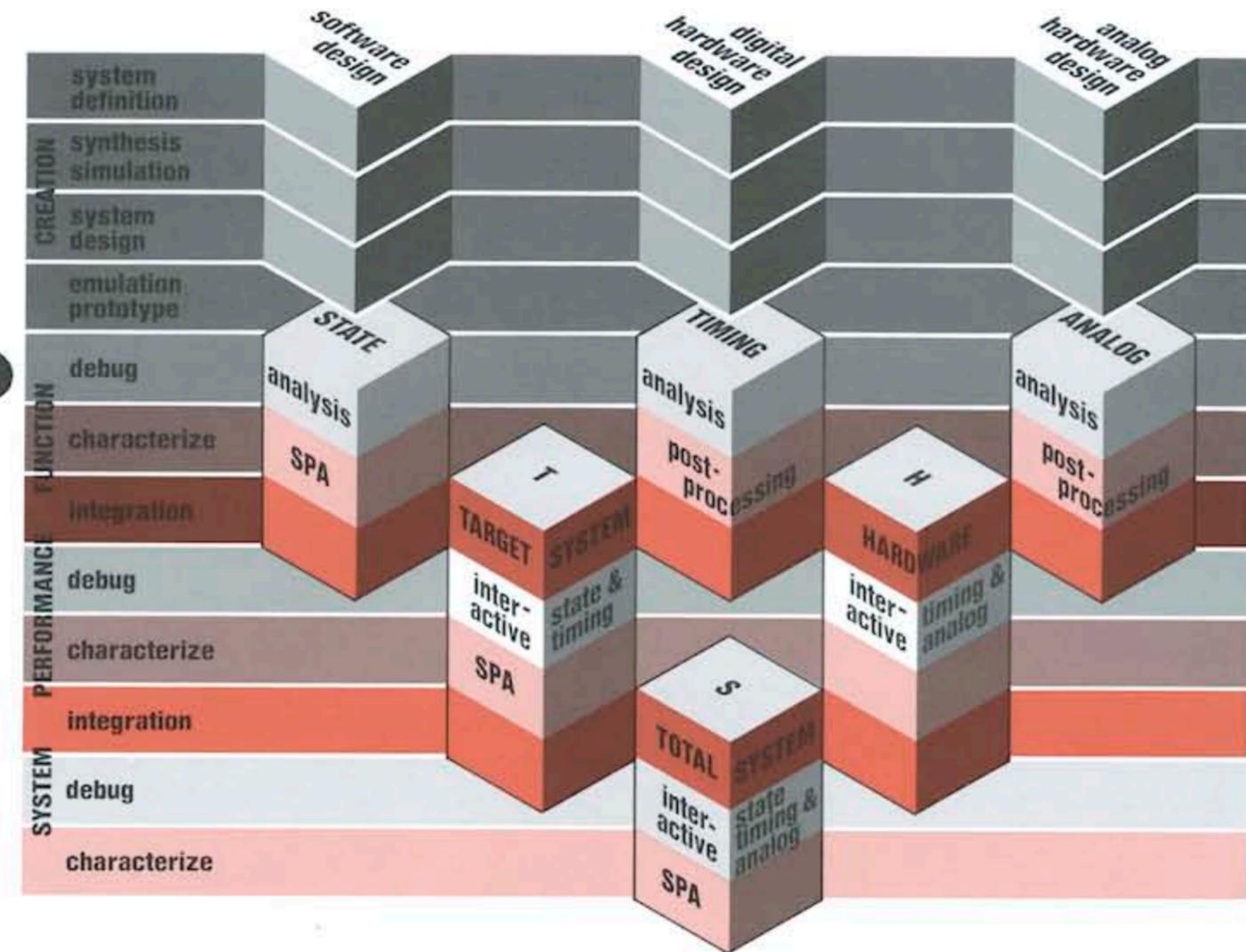
- Simultaneous acquisition and display of timing waveforms and analog waveforms with time correlation
- Timing pattern specification used for capturing analog waveforms
- Analog trigger specifications used for capturing timing patterns
- Two simultaneous time interval measurements
 - Interactive cursor pairs for correlating events

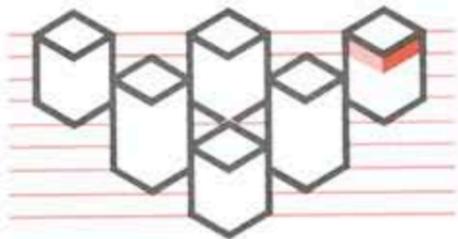
SYSTEM PHASE

System Integration, Debug, And Final Characterization

The design of a digital system evolves through three iterations of debug, characterization, and integration. The HP 1631A/D provides the user with tools to examine performance at 1) the global level (system performance analysis), 2) the functional level (state and timing analysis) and 3) the parametric level (analog waveform analysis). Then the user can use that information to guide total system debug with interactive measurements and selective debug with post-acquisition triggering.

The Design Of A Digital System





ANALOG WAVEFORM ANALYSIS

Combines Basic Oscilloscope Capability With Single-shot Digitizing



[] indicates parameters that can be changed using NEXT and PREV keys.

Traces can be continuous or single-shot. Continuous traces can be displayed single-shot or cumulative.

Sample period selection (5 ns - 500 ms) is used to control time per division. Total acquisition time is displayed.

Post-processing allows access to the menu that defines X and 0 cursors. To make statistical measurements, the HP 1631A/D must be in a continuous trace mode. Mean, and standard deviation, and maximum and minimum statistical values are displayed.

Assigned labels automatically appear on appropriate channels.

Probe attenuation automatically sets full-scale voltage limit for probe type selected (X1, X10, X50).

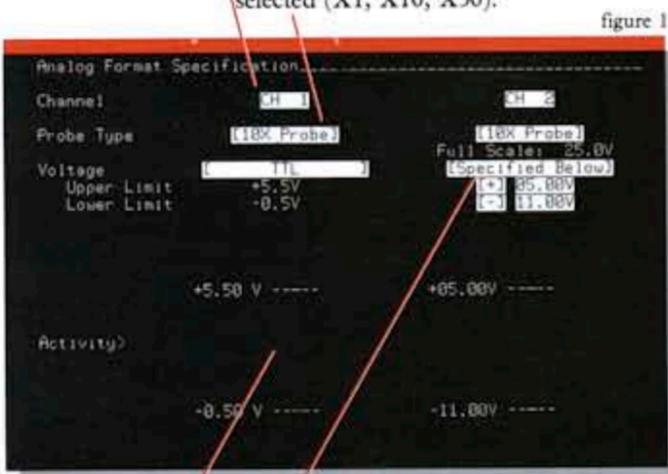


figure 1

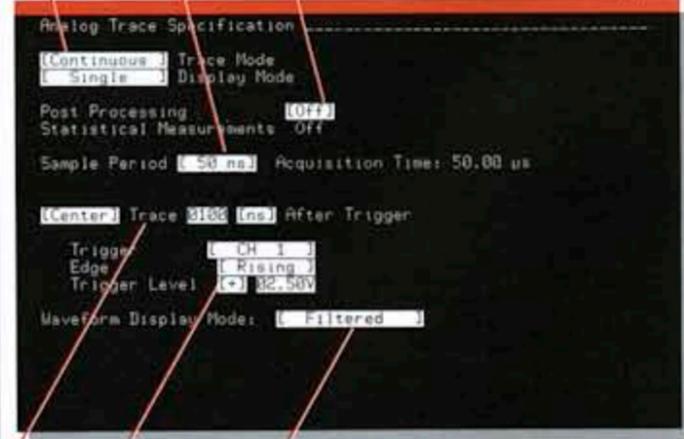


figure 2

The activity indicator shows the dynamic range of the input signal, which aids in selecting the correct upper and lower voltage limit for maximum viewable signal height.

User can specify voltage range and offset by setting upper and lower limits to the user-specified range or by selecting TTL or ECL presets.

Filter selection allows stair-step interconnection of real samples [Off], or uses a data interpolator that calculates and displays up to 19 points between samples [On].

Allows selection of trigger source: channel one, two, or external, plus rising or falling edge, and trigger voltage level.

Tracepoint equals trigger plus delay; tracepoint can be placed at start, center, or end of acquisition.

Magnification is selectable from 1 to 40 and can be centered about either cursor. Independent control of X and 0 cursors provides time-interval measurements. Answers are displayed.

Allows choice of user access to trace specification while monitoring display or cursor control, magnification, and readout of volts or time by pushing NEXT and PREV keys.

Sample period selection (5 ns - 500 ms) is used to control time per division. Total acquisition time is displayed.

Advisory indicates status of trigger in slow single-shot mode (low repetition rate). It also guides cursor-correlated interaction after a single-shot measurement is complete.

Runs indicates the total number of acquisitions.

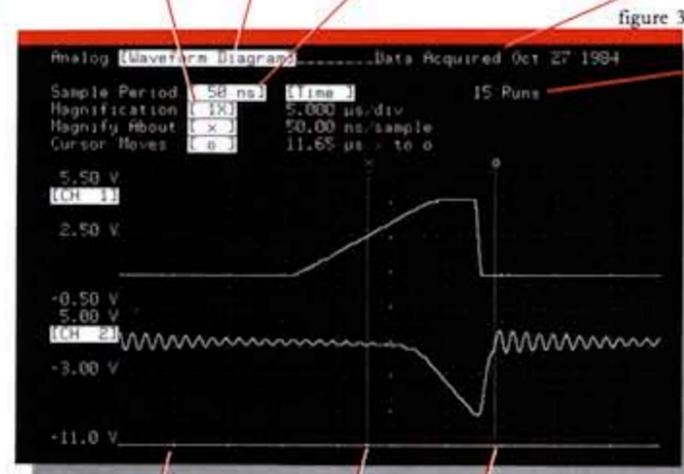
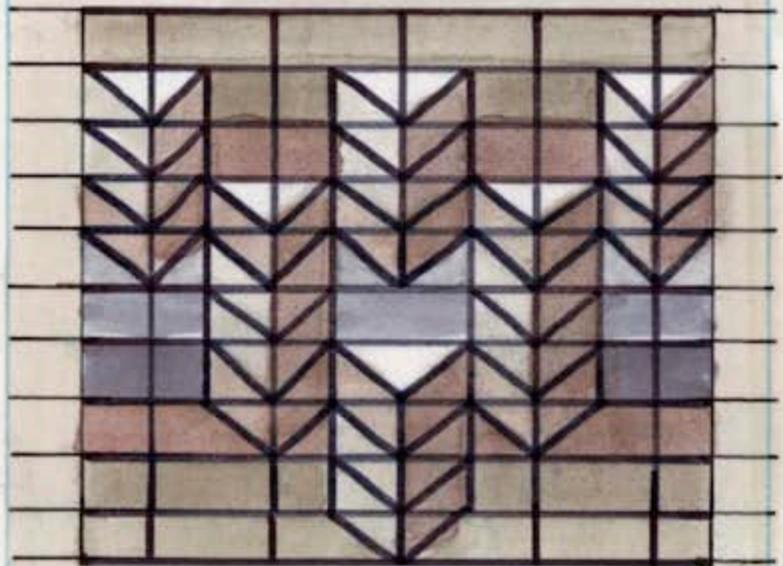
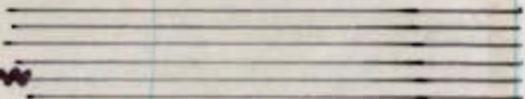


figure 3

Cursors can be moved together with a single control for time interval comparison measurements. Time-interval (X to 0) answers are displayed.

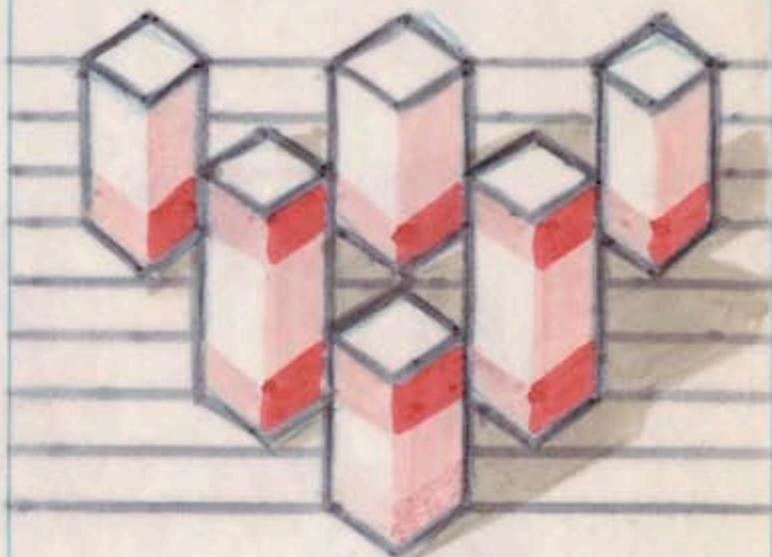
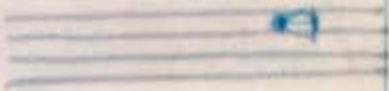
Indicates portion and location of memory displayed on-screen. Also displayed are the location of X (above the line) and 0 (below the line) cursors—especially helpful when cursors are off-screen.

Wavy lines



Wavy lines

Wavy lines



Wavy lines

W.C. SHADOW

6-454

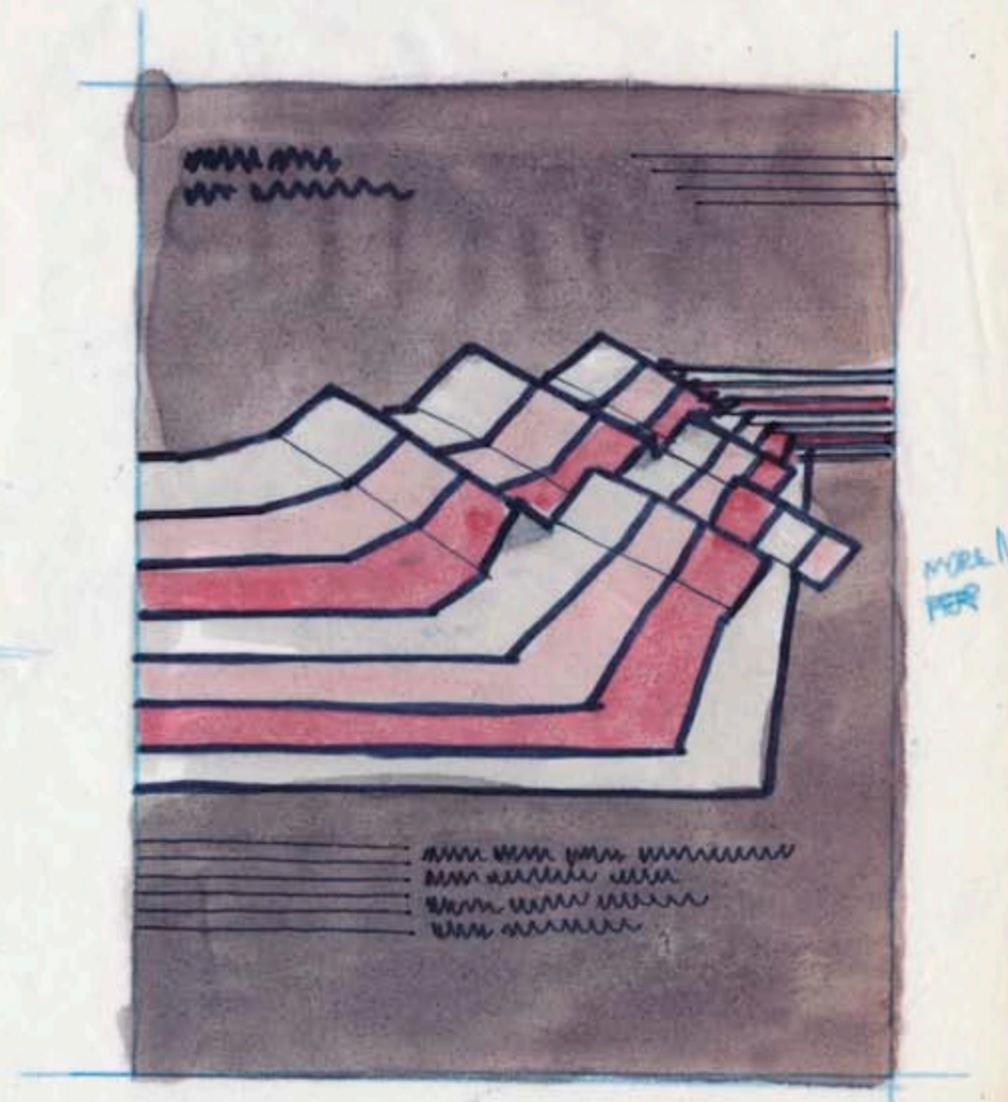
CONT ON BACK CONT

A hand-drawn diagram of a document layout. At the top, there are five horizontal lines with wavy patterns underneath, resembling musical notation. A blue box with the number '18' is positioned to the right of these lines. Below this section are two red horizontal bars. The first bar contains the text '183 / 190' and the second bar contains '185 / 192'. Below these bars are two more horizontal lines. At the bottom of the diagram, there is a section labeled '454' with musical notation below it. A blue arrow on the left side points upwards and is labeled 'CONT ON BACK CONT'. A blue 'X' is marked on the right side of the diagram.

1
400-405
440-434
438, 440

CONT. w/
TABLE OF CONTENTS

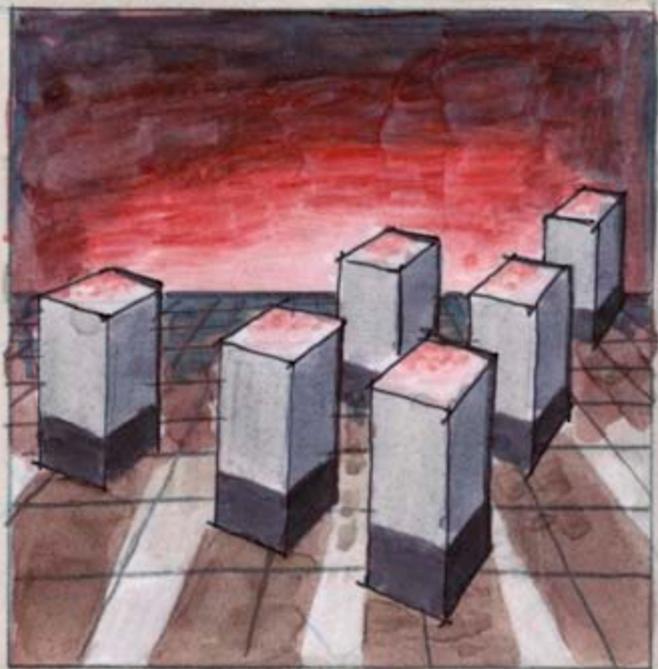
A hand-drawn diagram of a document layout. At the top, there are two horizontal lines with wavy patterns underneath. To the right, there are five horizontal lines. Below these lines is a large, complex red scribble with black lines overlaid on it, resembling a stylized signature or a large mark. Below the scribble, there are five horizontal lines with wavy patterns underneath. A blue arrow on the left side points upwards.



Wavy lines in black ink at the top left of the drawing.

MORE
PER

Wavy lines in black ink at the bottom of the drawing.



FLATTER
HOUSE VP